Firmware Control sheet

	Drg	**Pixie Switcher hex**
	Description	

Issue	*ECN*	*Date*	*Reason for Change*
1	n/a		**First Issue**

Device	**18LF4620**
R.F. Sols. ref.	

Design Requirements		
Feature	**(Default)**	**Design / Customer Requirement**
Ident. Mark	*Dot*	
Oscillator		**INT RC, RA6, RA7 ports**
Fail safe clock mon		**Disabled**
Oscillator Switch Enable		**Disabled**
Power Up Timer		**Disabled**
B.O.D.		**Enabled in hardware**
B.O.Voltage.		**2.0V**
Watchdog		**Disabled,**
Watchdog Postscaler		**1:4096**
CCP2 Mux		**RB3**
Port B A/D Enable		**Digital I/O on reset**
Low power timer 1		**Disabled**
Master Clear Enable		**MCLR Enabled**
Stack Overflow Reset		**Enabled**
LV Program		**Disabled**
Code Protect		**All enabled (code protected)**
Data EE Protect		**Disabled**
Read protect		**All Disabled**
Write protect		**All Disabled**
B'gnd. debug		**Disabled**

Serialisation?		*No*	
Type		*Random*	
Start Address		*$*	
No of Words		*decimal -*	

Checksum	

Other Requirements	