




## Firmware Control sheet

	<b>Drg</b>	<b>PixieMAC hex</b>	
	<b>Description</b>	Config bits apply to PXMC, PXMF, PXMS builds	
<b>Issue</b>	<b>ECN</b>	<b>Date</b>	<b>Reason for Change</b>
<b>1</b>	<b>n/a</b>		<b>First Issue</b>

<b>Device</b>	<b>18LF4620</b>
<b>R.F. Sols. ref.</b>	

Design Requirements			
Feature	(Default)	Design / Customer Requirement	
<b>Ident. Mark</b>	<b>Dot</b>		
<b>Oscillator</b>		<b>HS</b>	
<b>Fail safe clock mon</b>		<b>Disabled</b>	
<b>Oscillator Switch Enable</b>		<b>Disabled</b>	
<b>Power Up Timer</b>		<b>Disabled</b>	
<b>B.O.D.</b>		<b>Enabled in hardware</b>	
<b>B.O.Voltage.</b>		<b>2.0V</b>	
<b>Watchdog</b>		<b>Disabled, controlled by SWDTEN</b>	
<b>Watchdog Postscaler</b>		<b>1:512</b>	
<b>CCP2 Mux</b>		<b>RB3</b>	
<b>Port B A/D Enable</b>		<b>Digital I/O on reset</b>	
<b>Low power timer 1</b>		<b>Disabled</b>	
<b>Master Clear Enable</b>		<b>MCLR Enabled</b>	
<b>Stack Overflow Reset</b>		<b>Enabled</b>	
<b>LV Program</b>		<b>Disabled</b>	
<b>Code Protect</b>		<b>Disabled</b>	
<b>Data EE Protect</b>		<b>Disabled</b>	
<b>Read protect</b>		<b>All Disabled</b>	
<b>Write protect</b>		<b>All Disabled</b>	
<b>B'gnd. debug</b>		<b>Disabled</b>	

<b>Serialisation?</b>		<b>Yes</b>	Contact FlexiPanel for a SQTP file of unique MAC addresses
<b>Type</b>			

<b>Checksum</b>	
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<b>Other Requirements</b>	
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